

FIG. 1 (PRIOR ART)

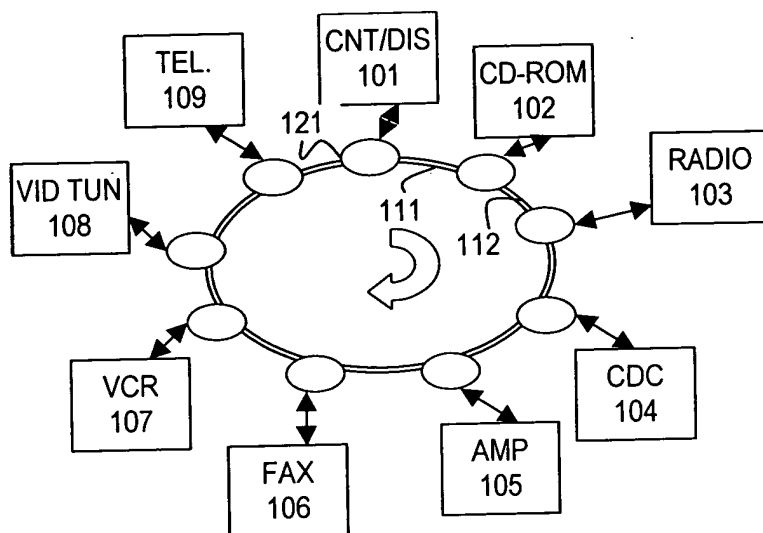


FIG. 2 (PRIOR ART)

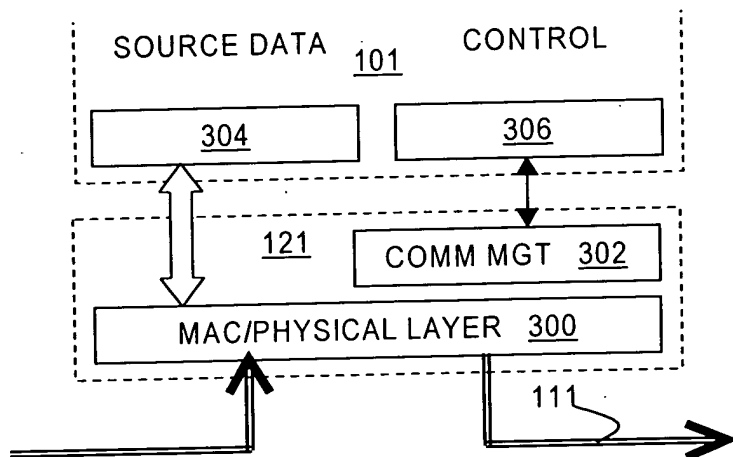


FIG. 3 (PRIOR ART)

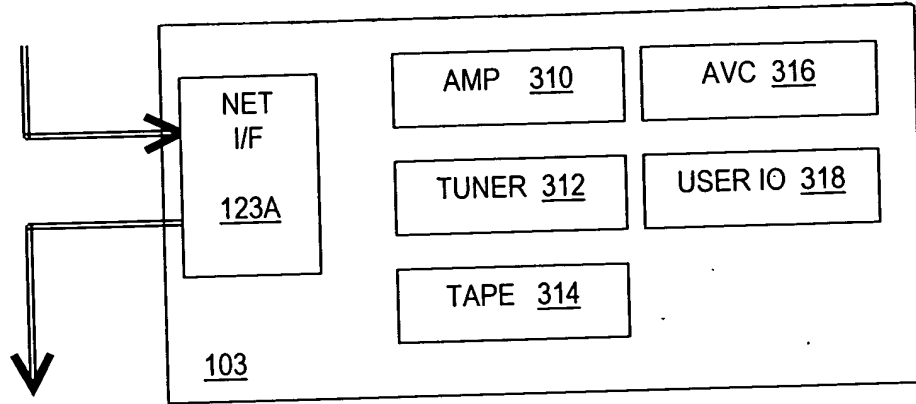


FIG. 4 (PRIOR ART)

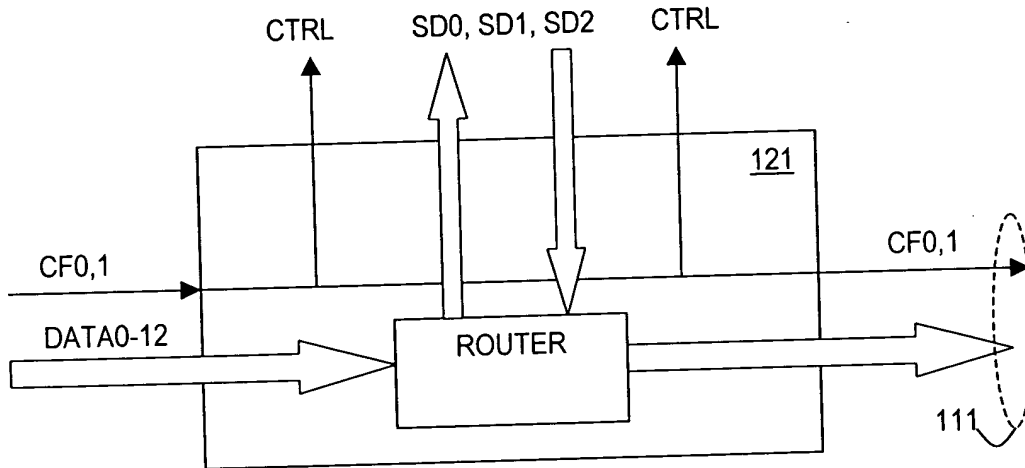


FIG. 5 (PRIOR ART)

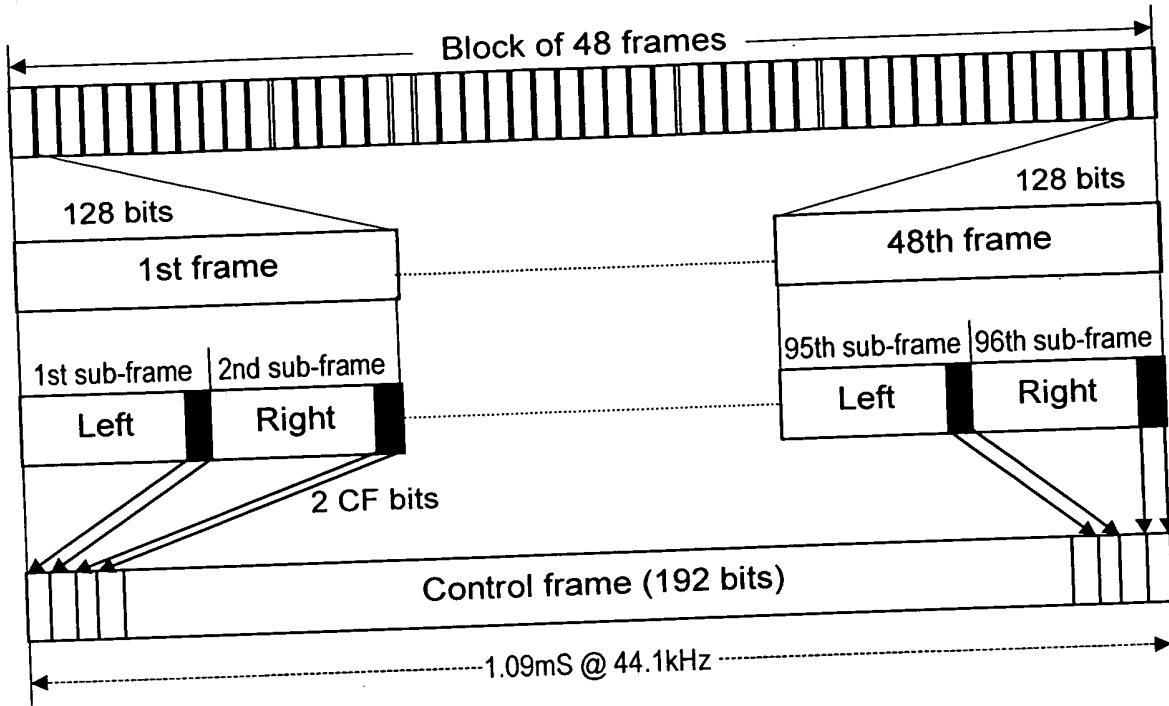


FIG. 6 (PRIOR ART)

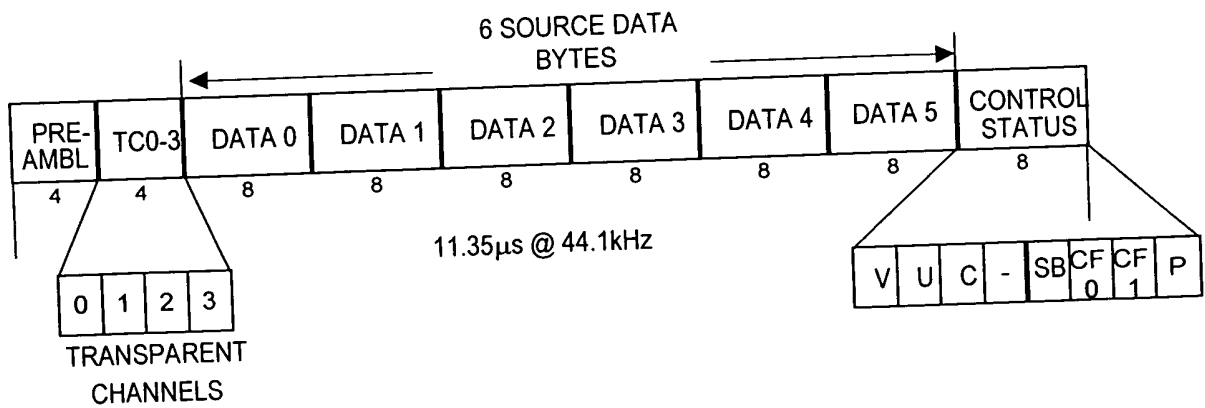


FIG. 7

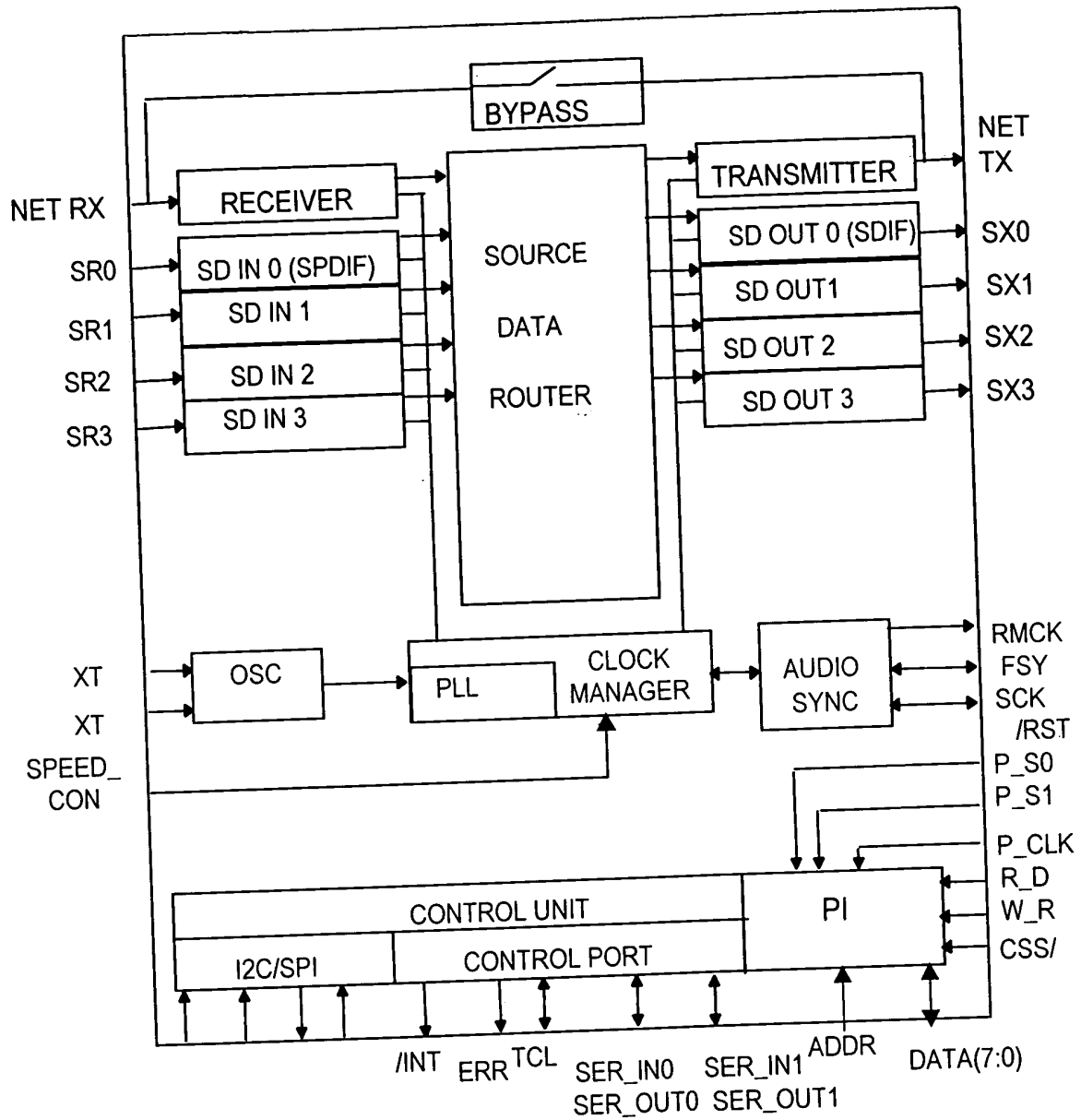


FIG. 8

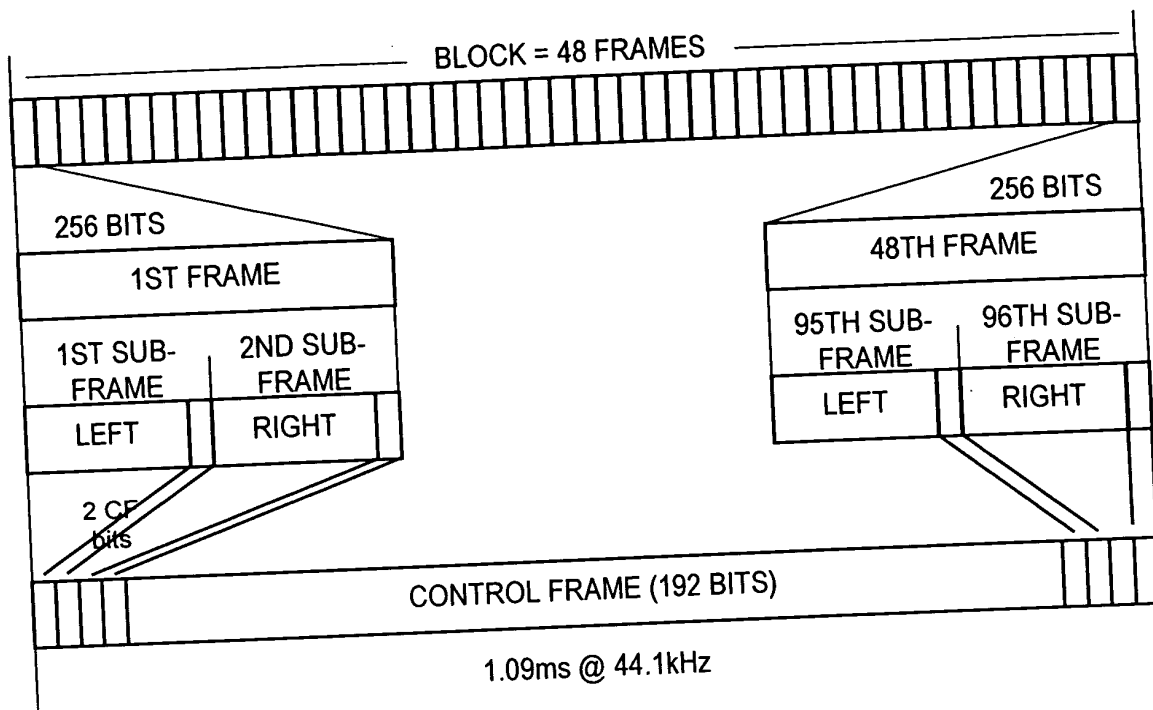


FIG. 9

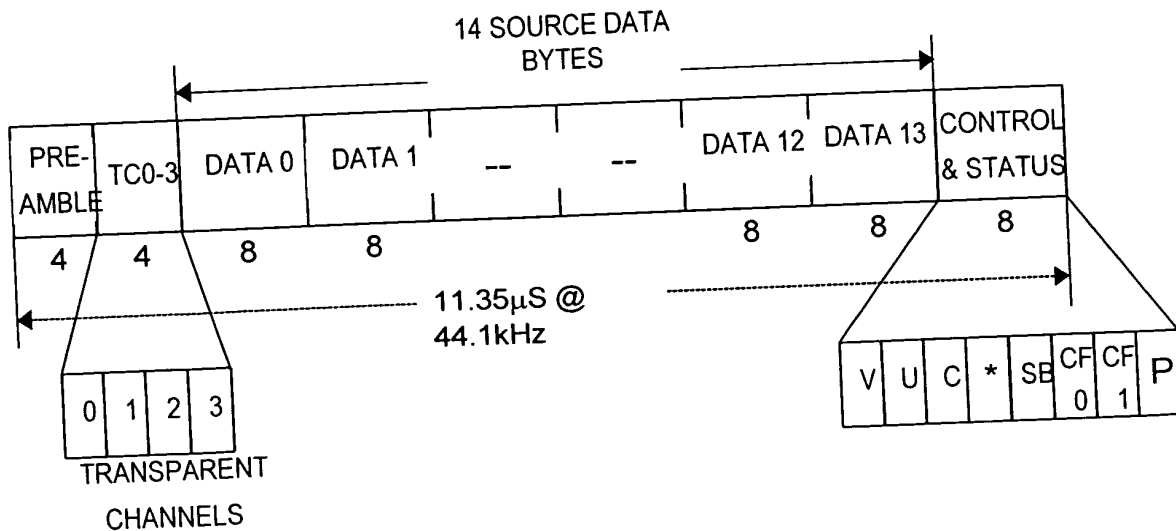


FIG. 10

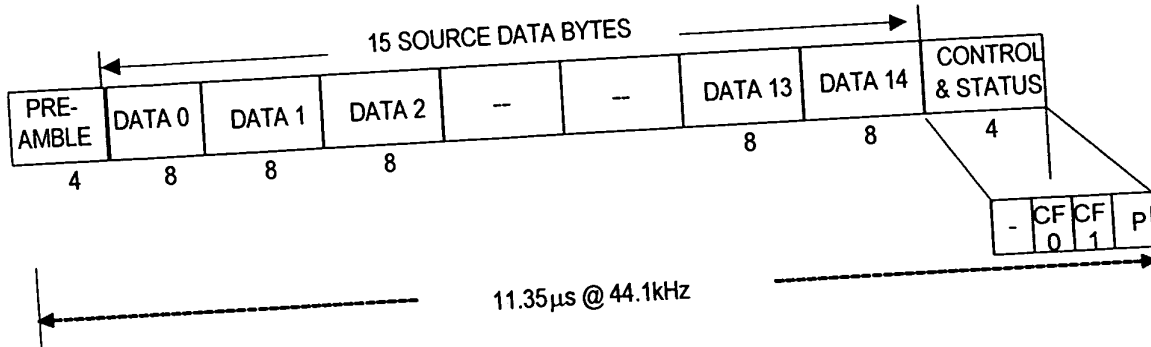


FIG. 11

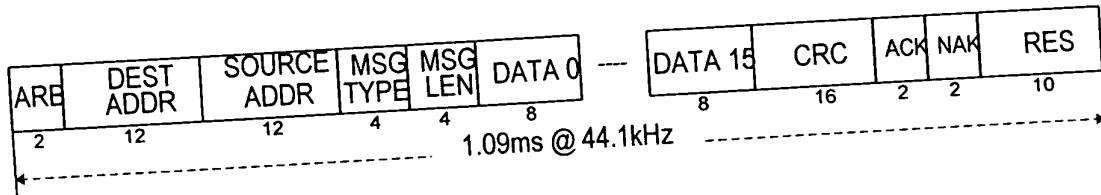


FIG. 12

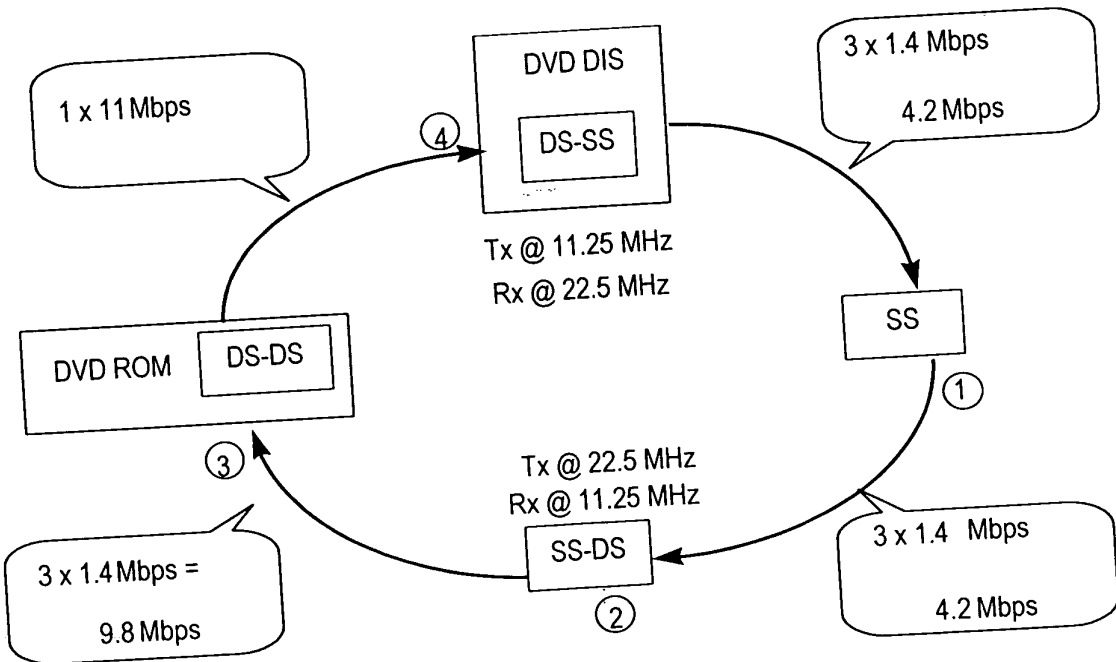


FIG. 13

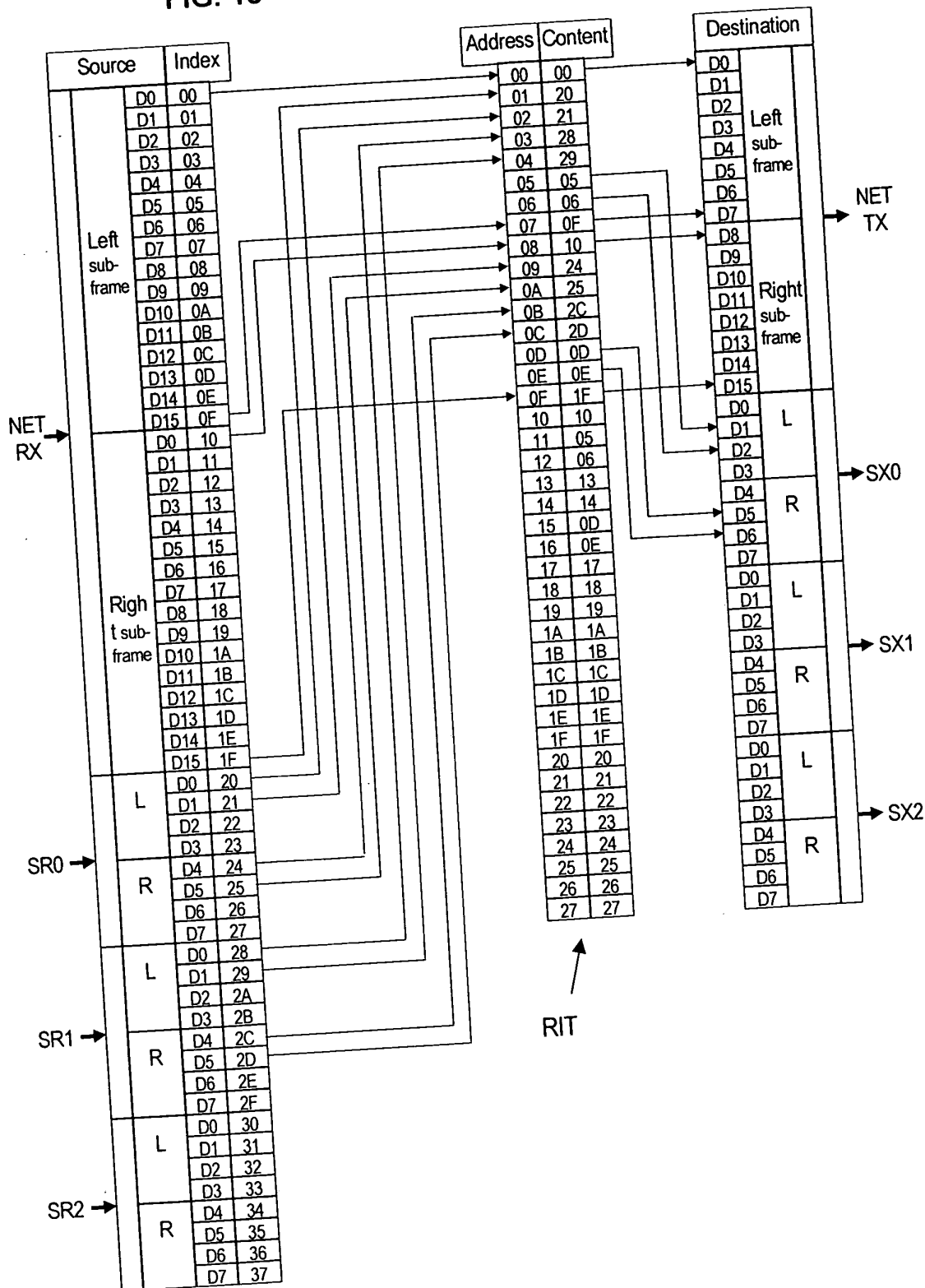


FIG. 14

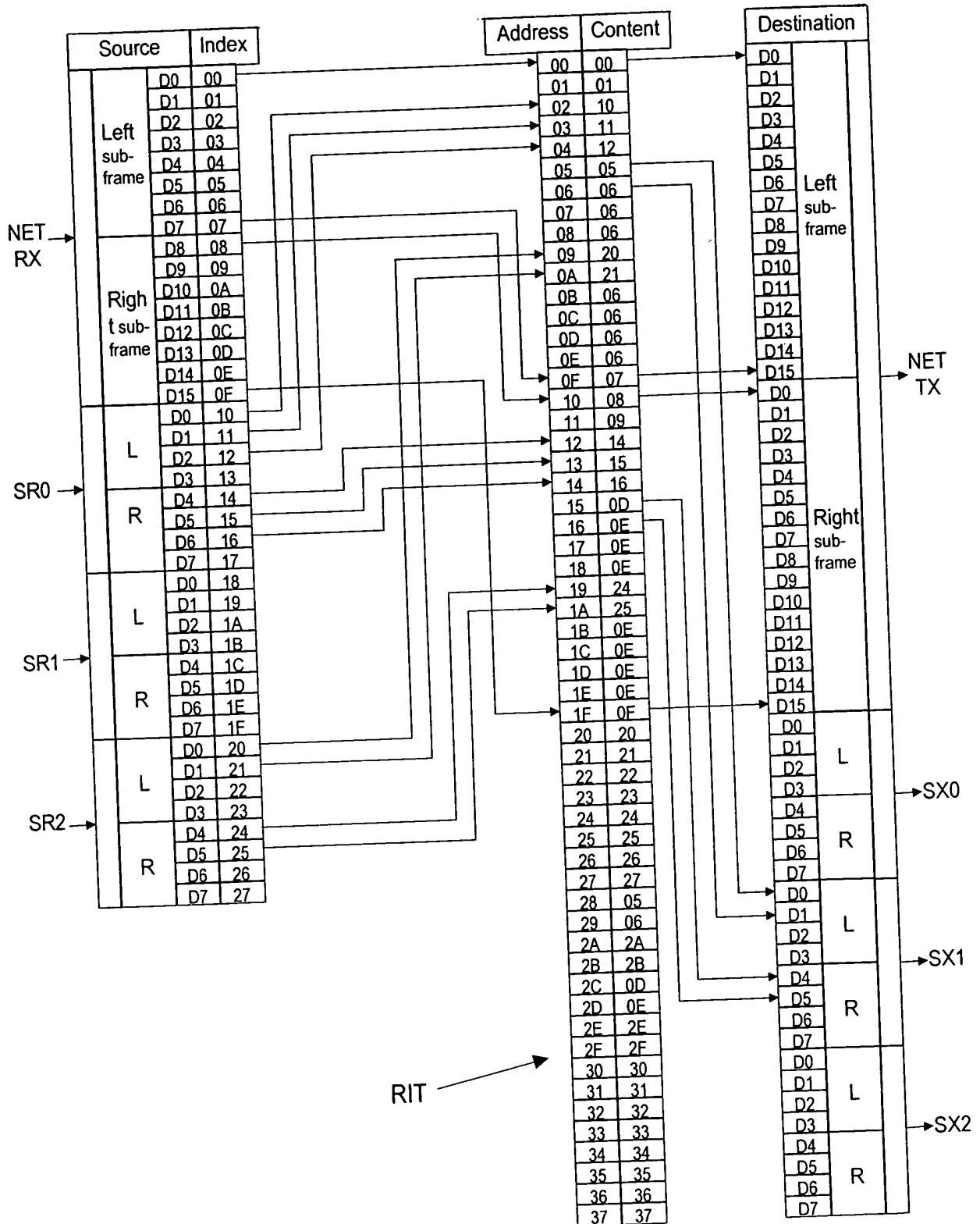




FIG. 15

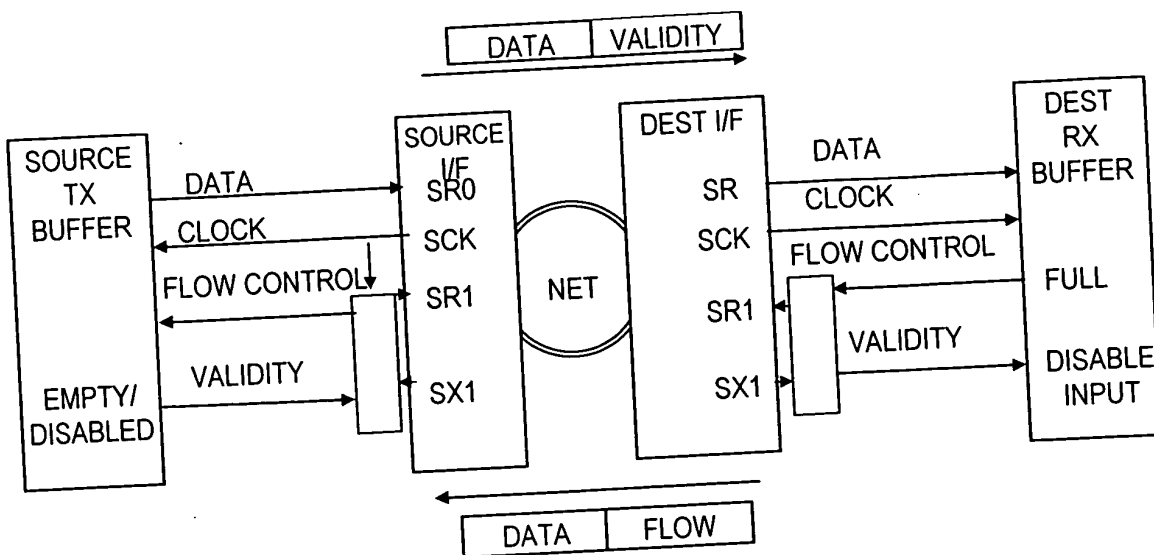


FIG. 16

SOURCE TO DESTINATION

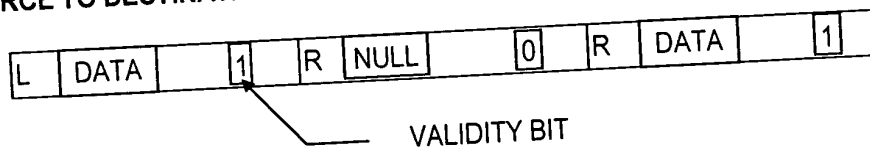


FIG. 17

DESTINATION TO SOURCE

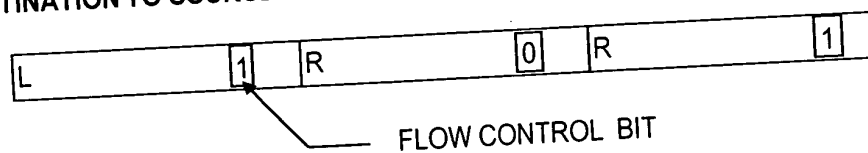






FIG. 20

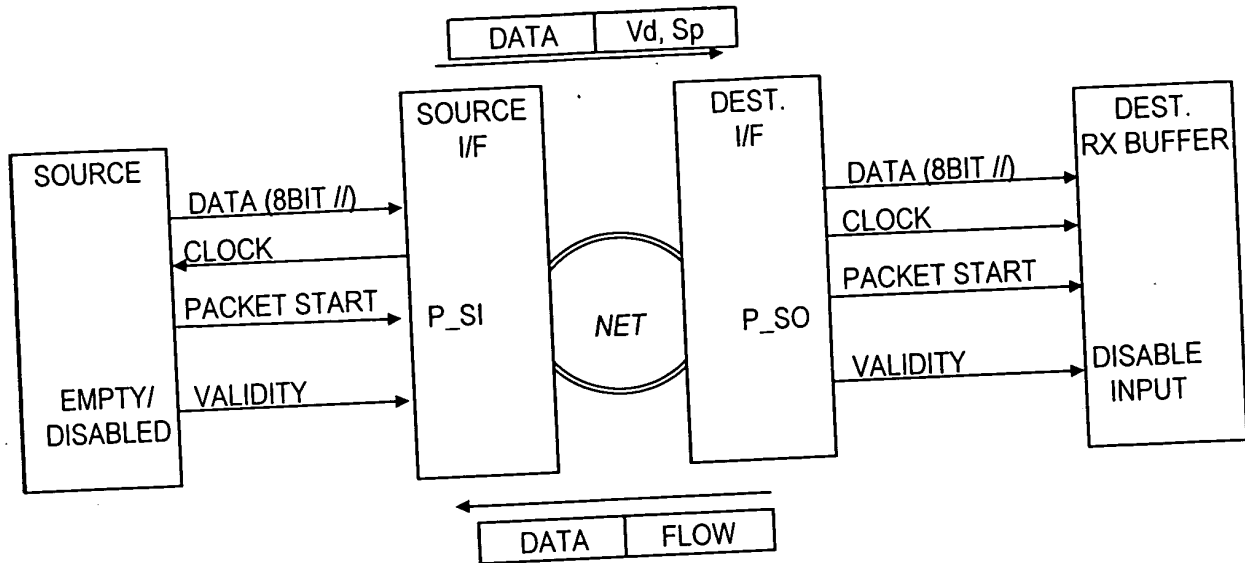


FIG. 21

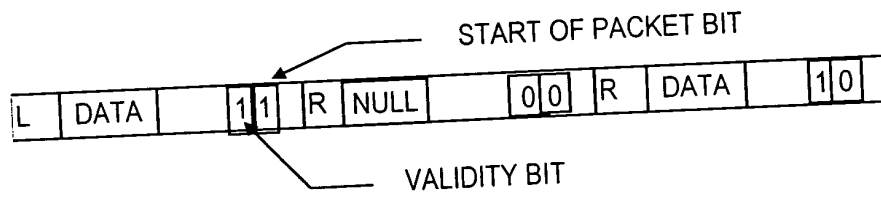
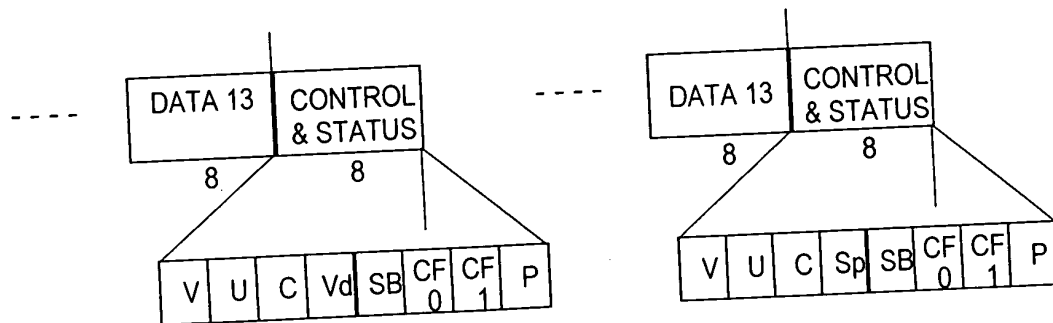


FIG. 22



(a) LEFT

(b) RIGHT

FIG. 23

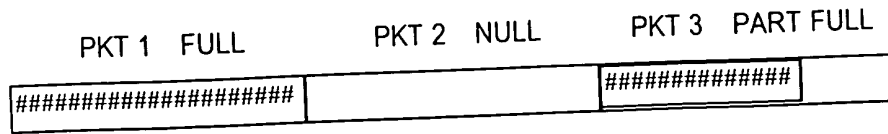


FIG. 24

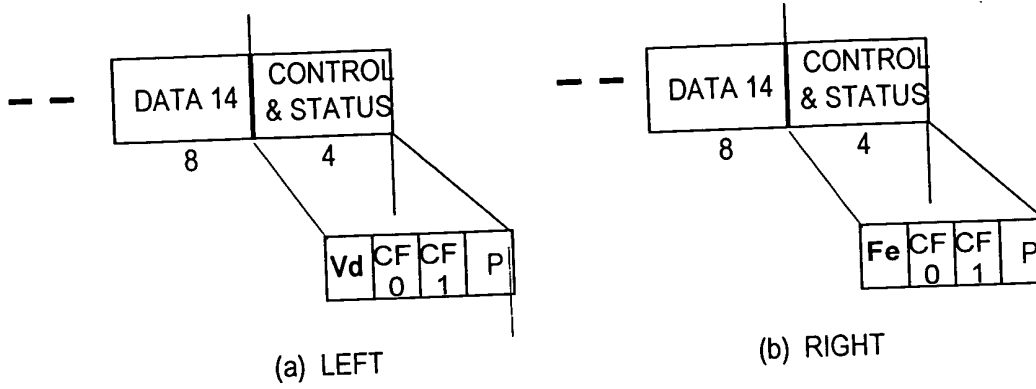


FIG. 25

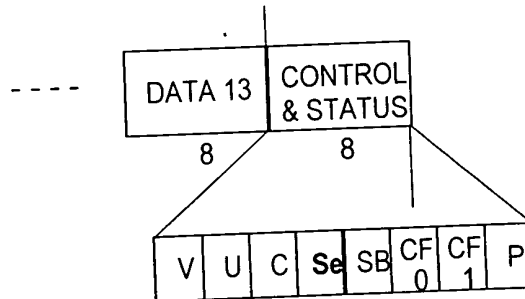


FIG. 26

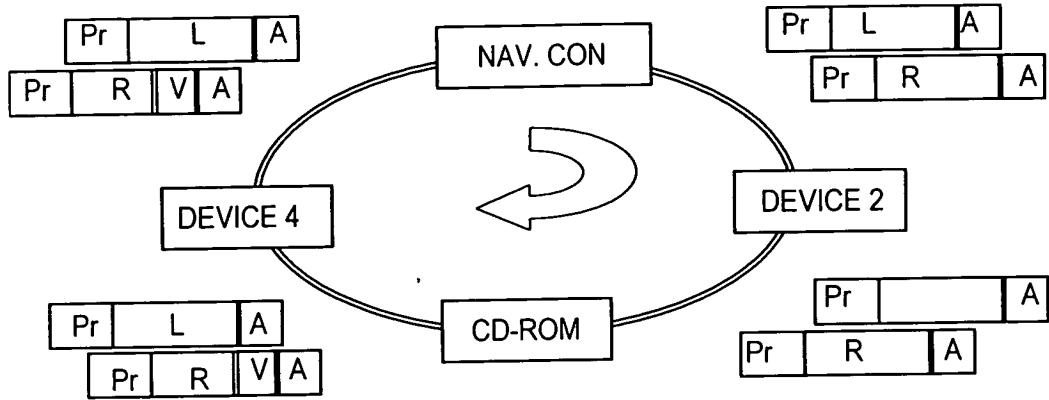


FIG. 27

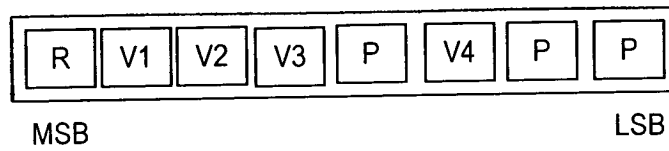


FIG. 28

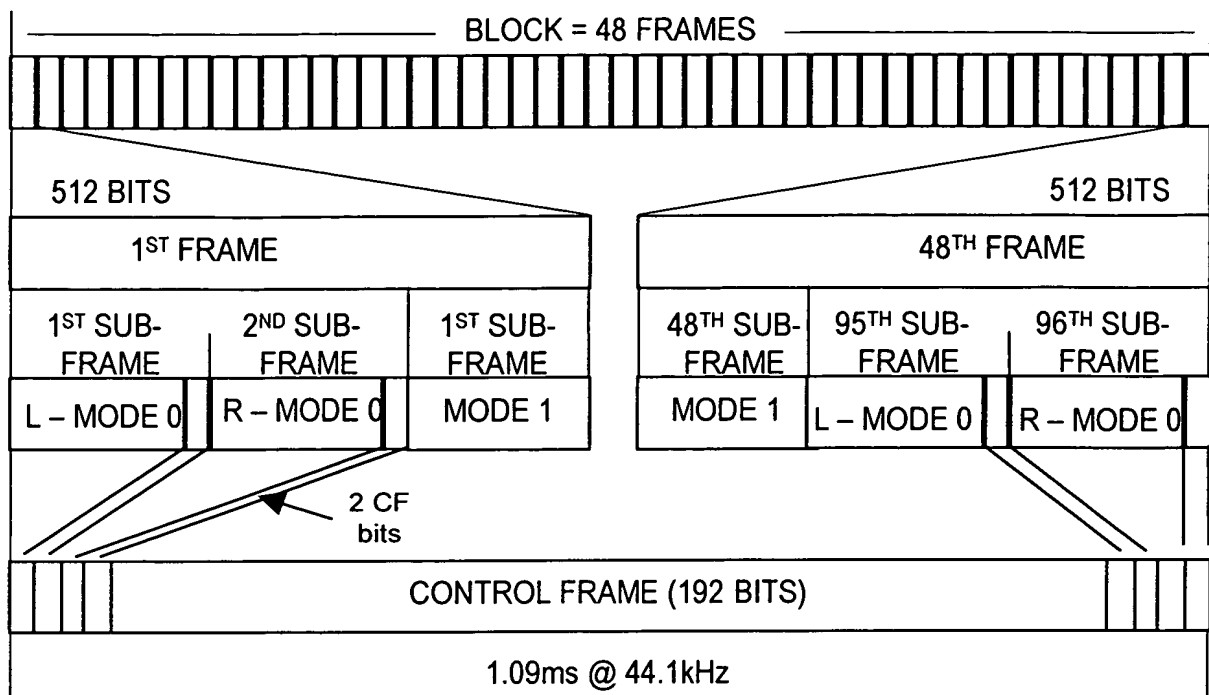


FIG. 29

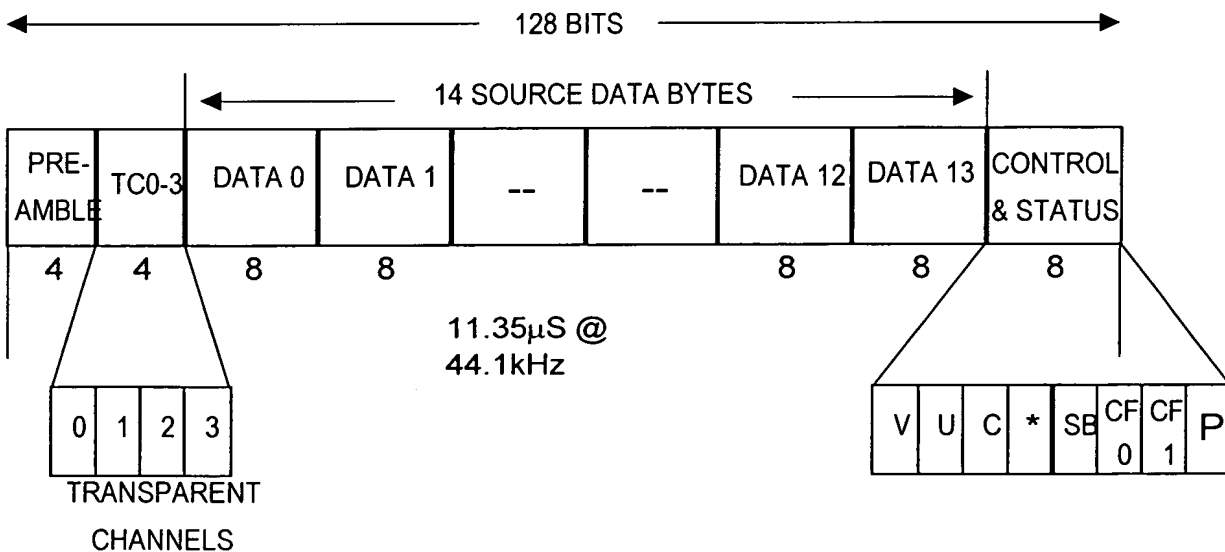
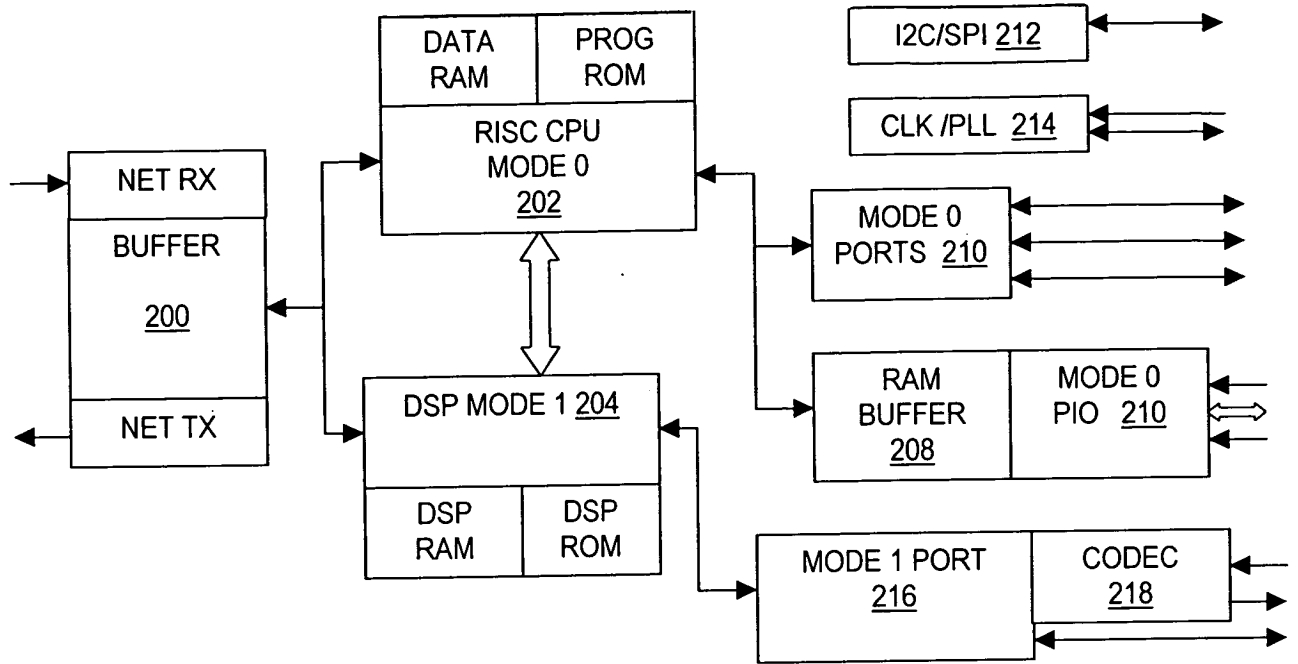


FIG. 30



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FIG. 31

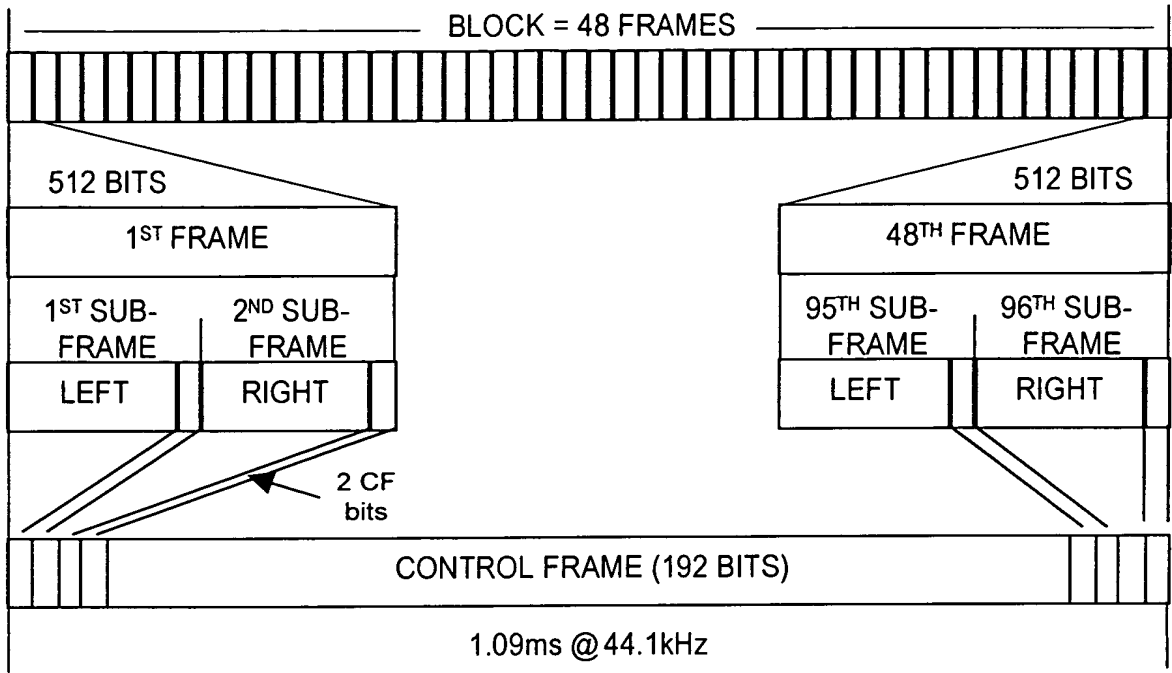


FIG. 32

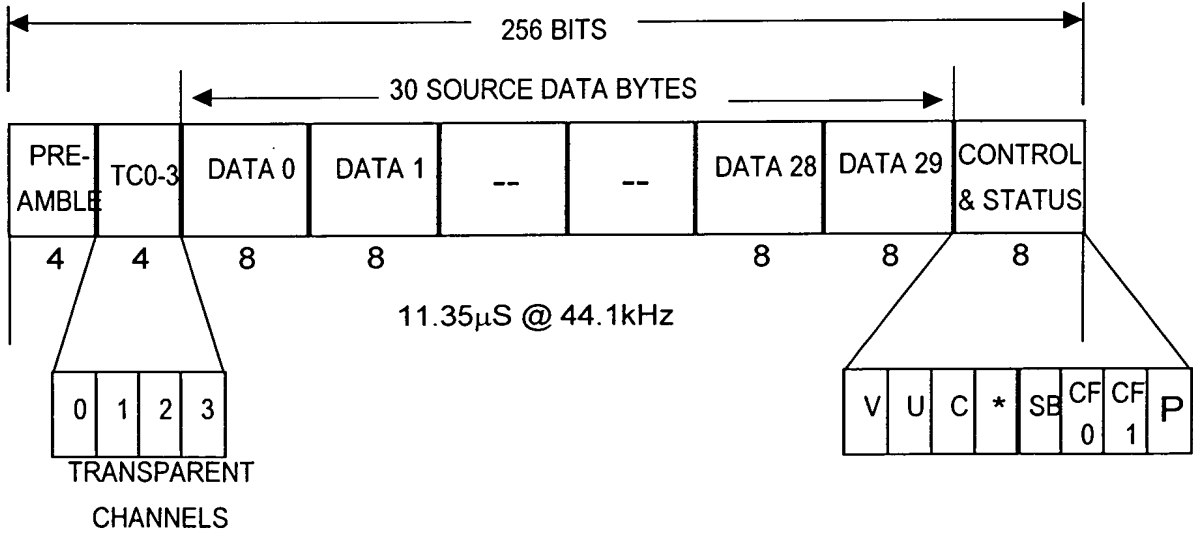




FIG. 34

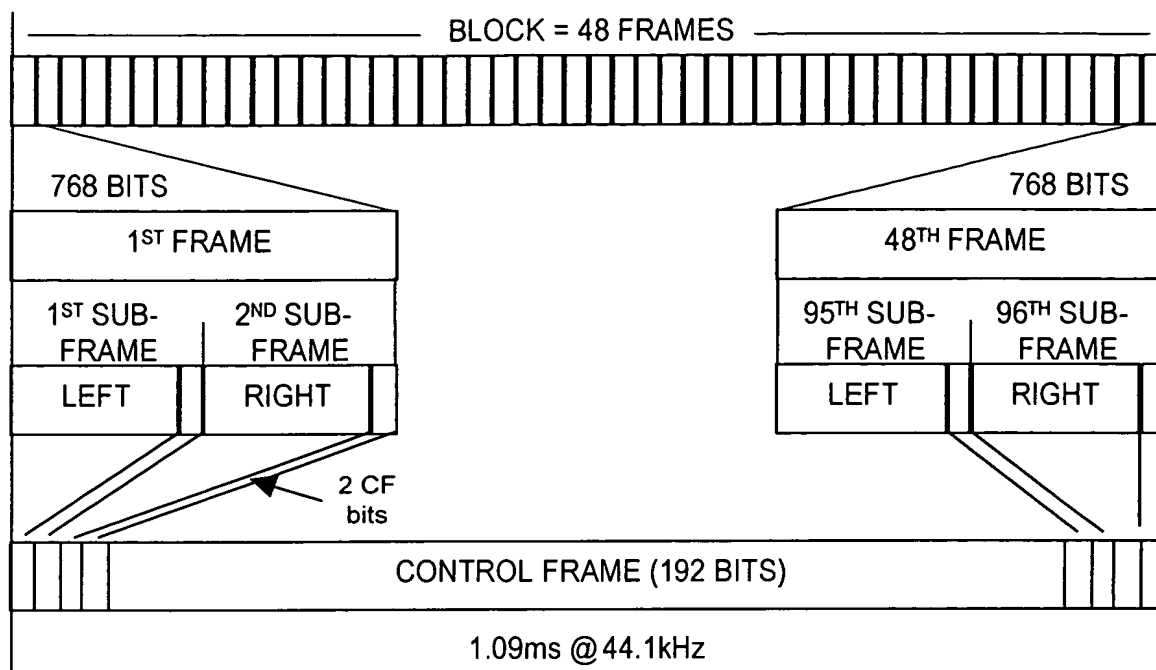


FIG. 35

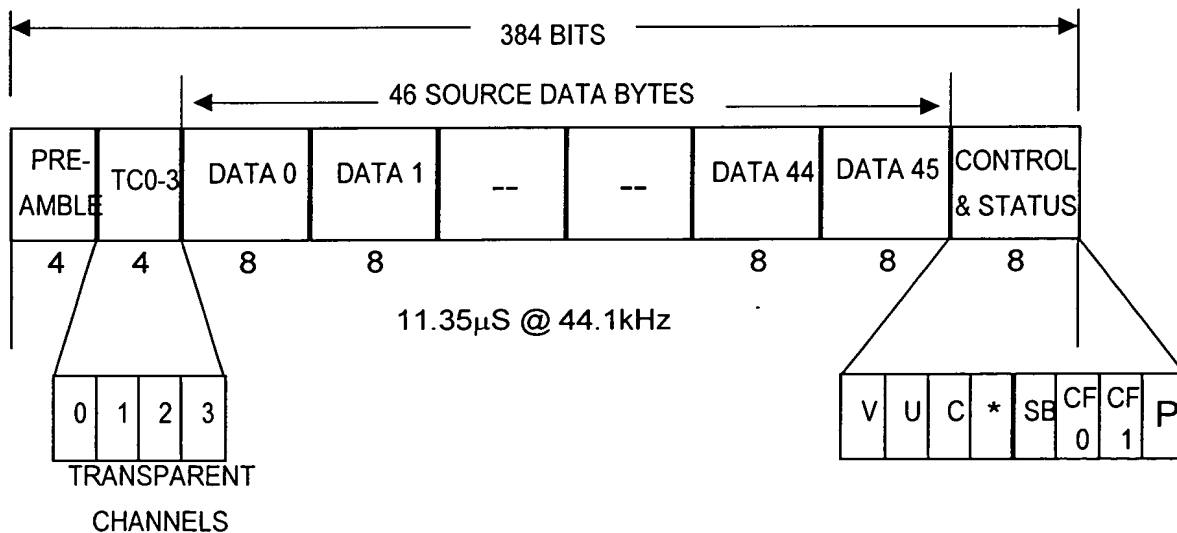


FIG. 36

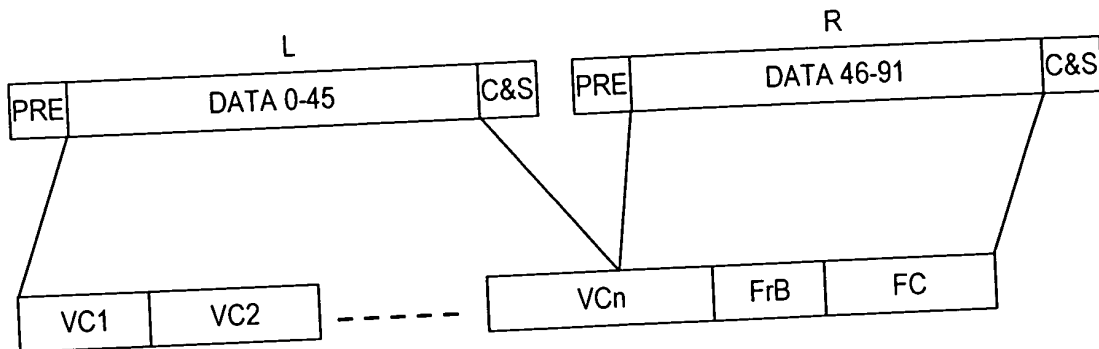


FIG. 37

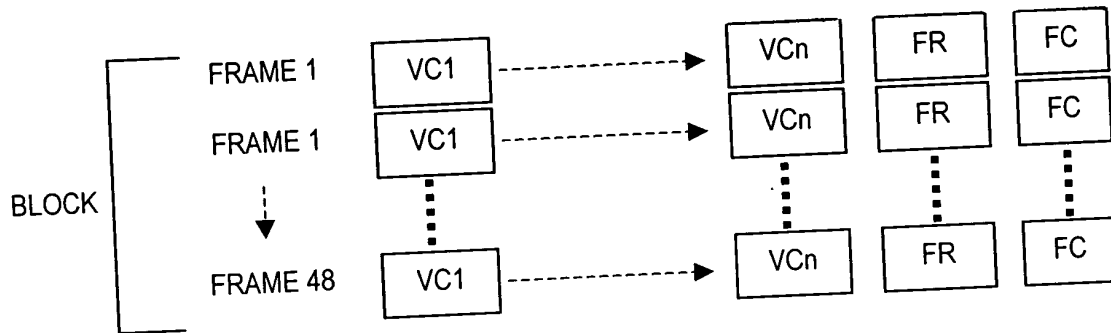
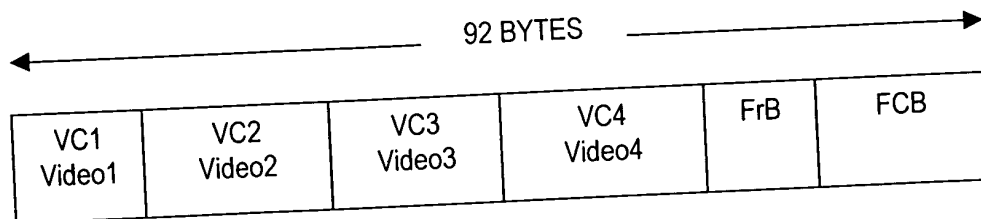


FIG. 38



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FIG. 39

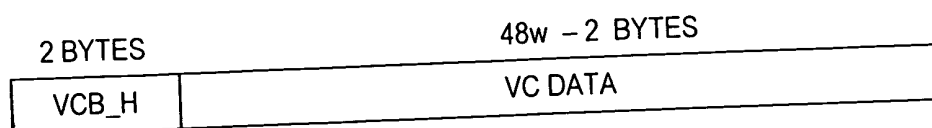


FIG. 40

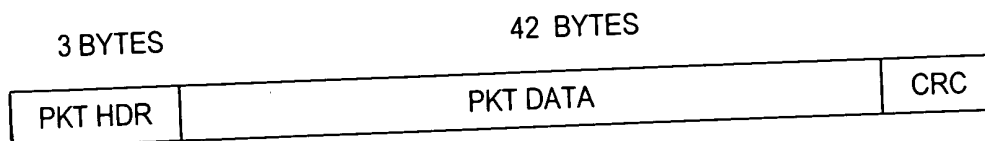


FIG. 41

The diagram illustrates the structure of VCB\_H and PA/PB registers for various frames (Fr.).

**VCB\_H Registers:**

- Fr. 1:** VCB\_H (16 bits), PA1 (8 bits), PA2 (8 bits), PA3 (8 bits), PA4 (8 bits).
- Fr. 2:** PA5 (8 bits), PA6 (8 bits), PA7 (8 bits), PA8 (8 bits), PA9 (8 bits), PA10 (8 bits).
- ...**
- Fr. 8:** PA41 (8 bits), PA42 (8 bits), PA43 (8 bits), PA44 (8 bits), PA45 (8 bits), PA46 (8 bits).
- Fr. 9:** R (8 bits), R (8 bits), PB1 (8 bits), PB2 (8 bits), PB3 (8 bits), PB4 (8 bits).
- Fr. 10:** PB5 (8 bits), PB6 (8 bits), PB7 (8 bits), PB8 (8 bits), PB9 (8 bits), PB10 (8 bits).
- ...**
- Fr. 16:** PB41 (8 bits), PB42 (8 bits), PB43 (8 bits), PB44 (8 bits), PB45 (8 bits), PB46 (8 bits).

FIG. 42

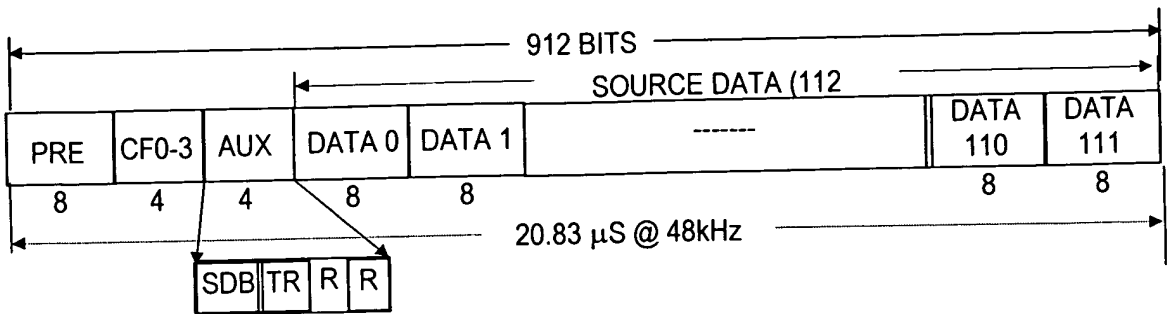


FIG. 43

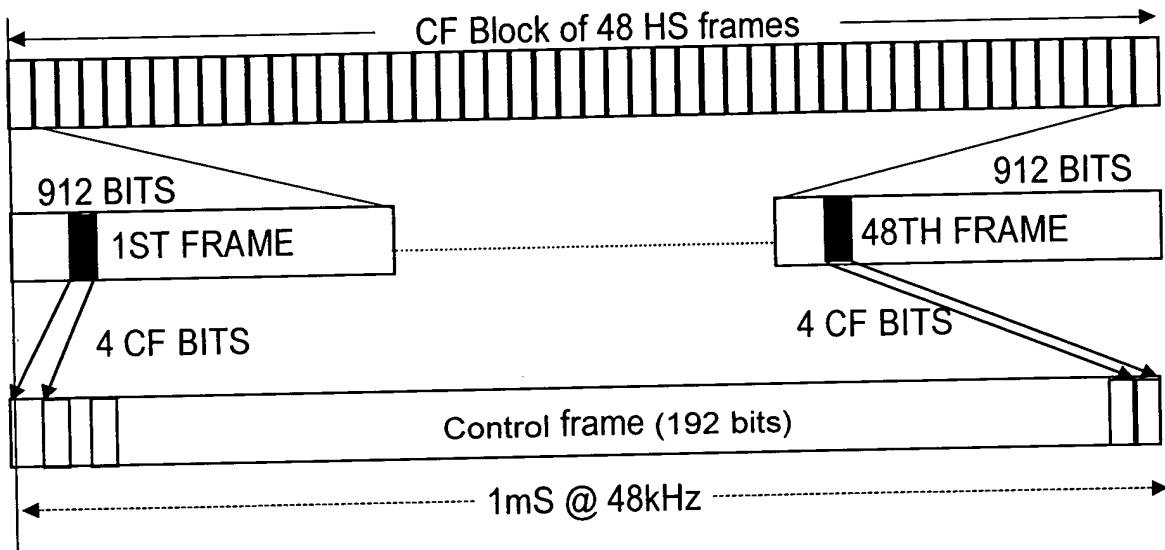


FIG. 44

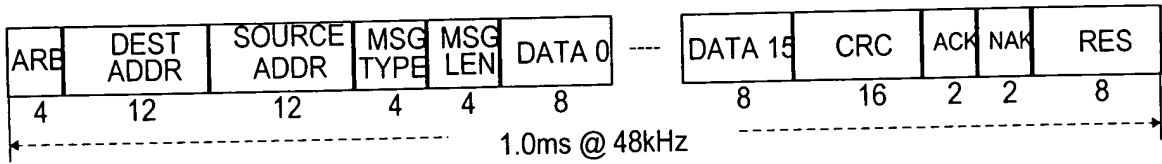


FIG. 45

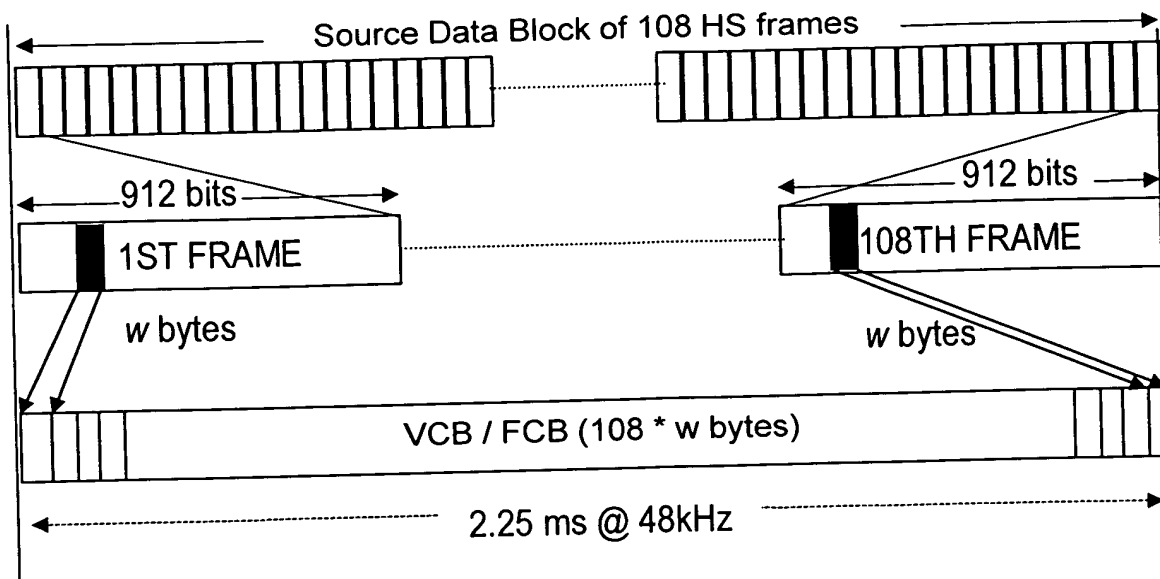


FIG. 46

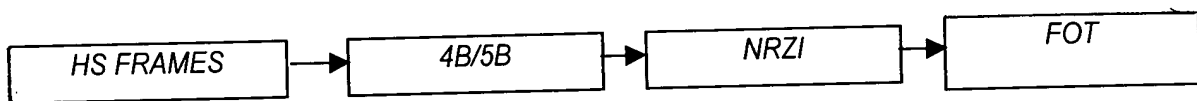
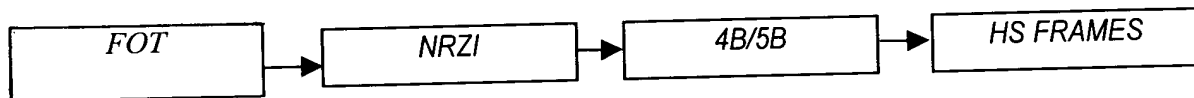


FIG. 47



66123 0932400

FIG. 48

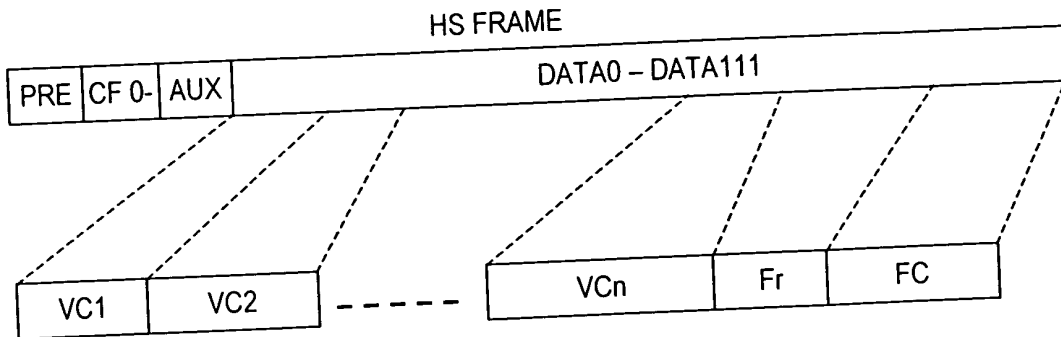


FIG. 49

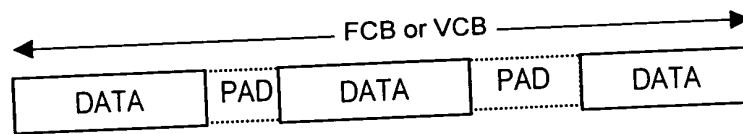


FIG. 50

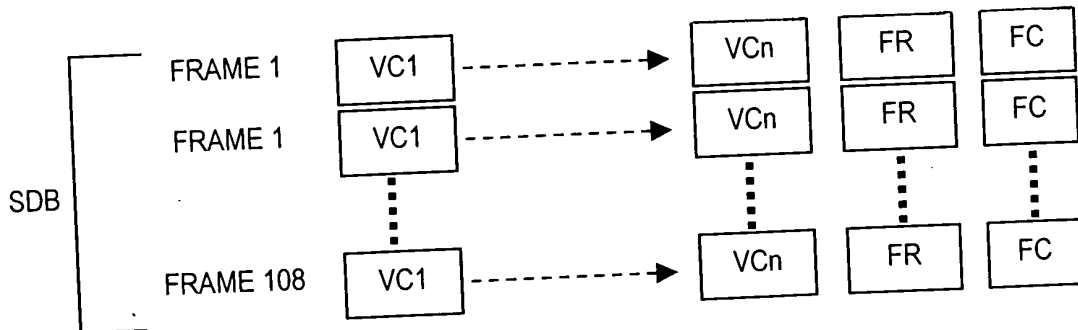
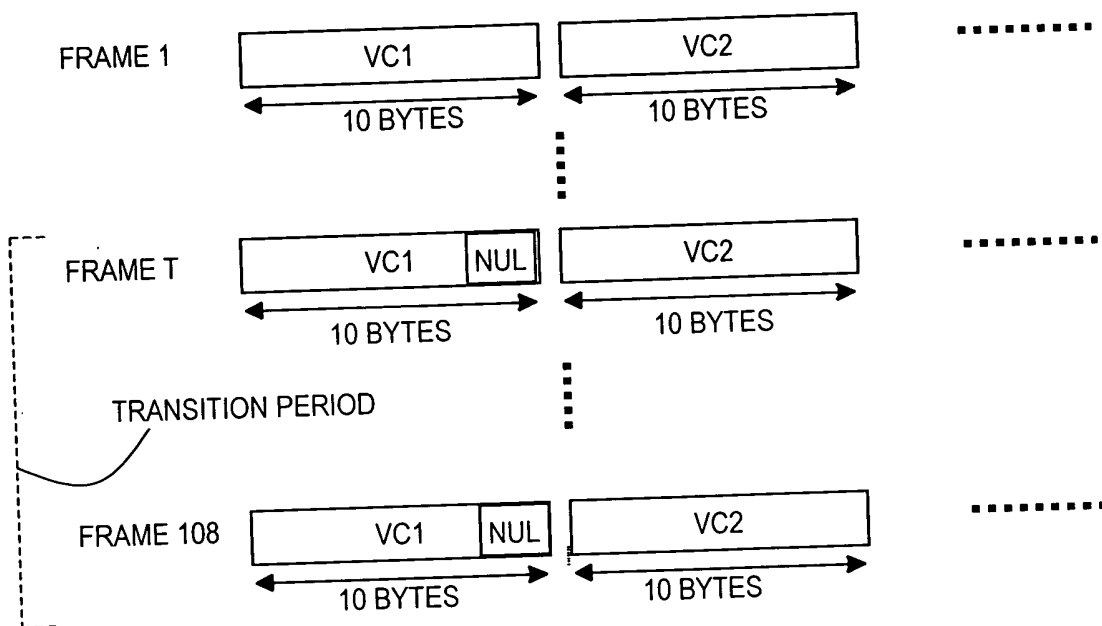
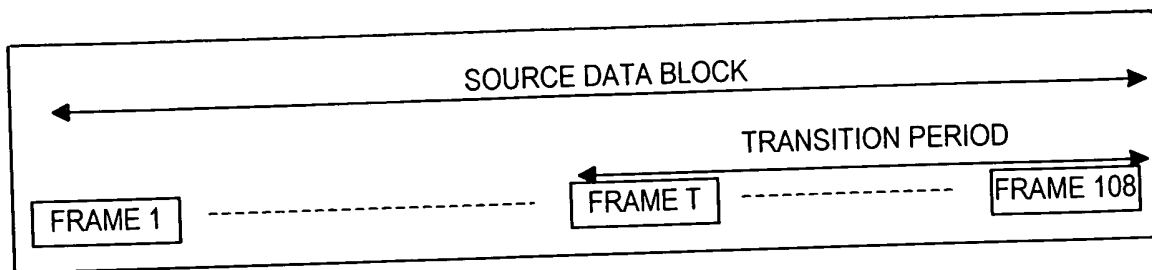






FIG. 54



NEW SOURCE DATA BLOCK

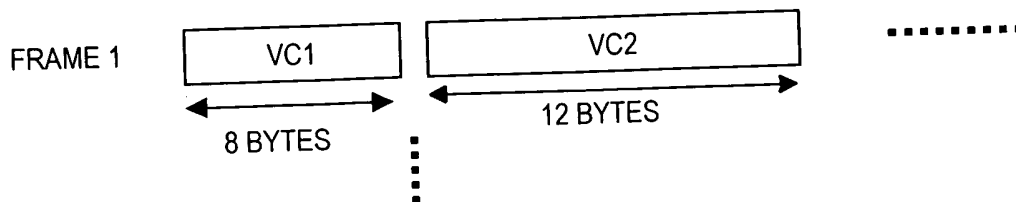


FIG. 55

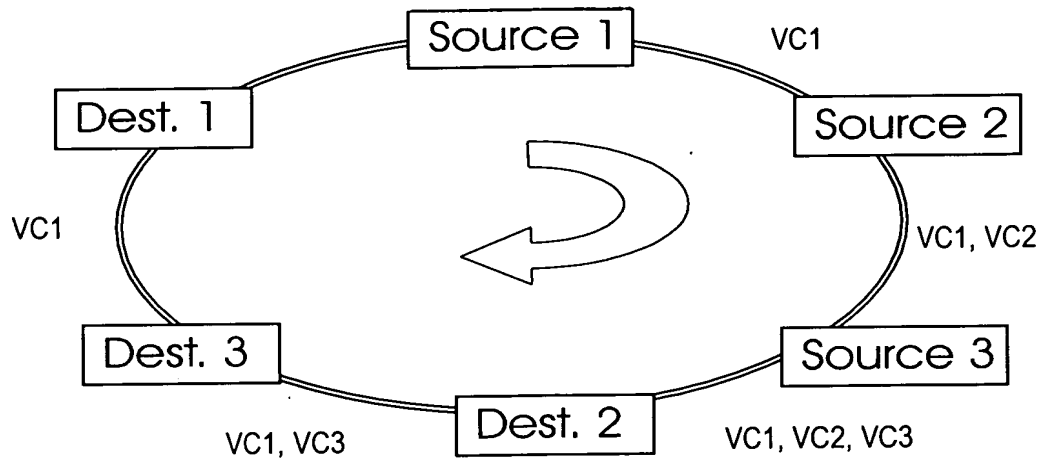
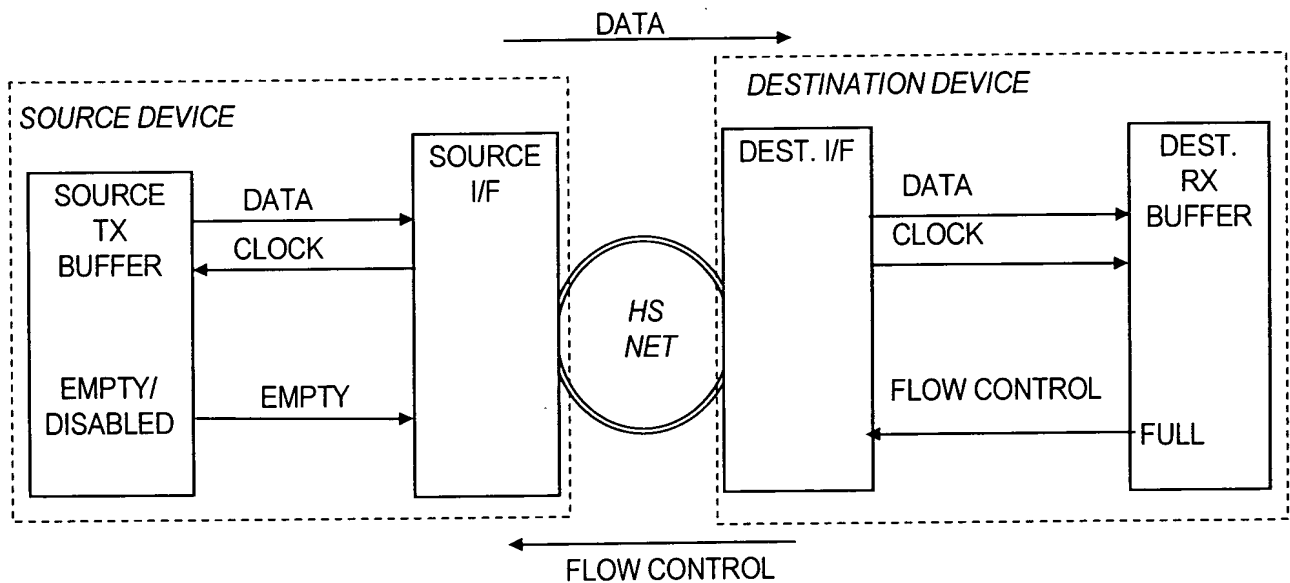


FIG. 56



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FIG. 57

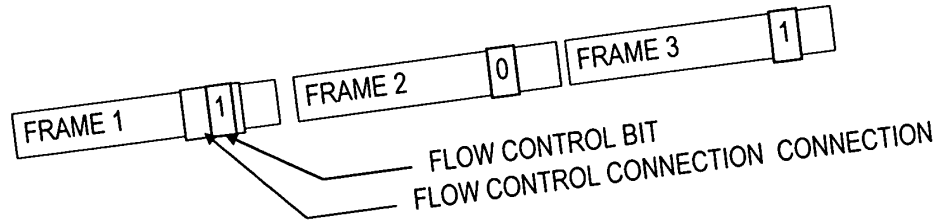


FIG. 58

